

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,400,996 B2
APPLICATION NO. : 10/606463
DATED : July 15, 2008
INVENTOR(S) : Benjamin Thomas Percer et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 1, line 7-9, below "RELATED APPLICATIONS" delete "The present application is related to the following commonly owned U.S. patent applications, incorporated in their entirety herein by reference:".

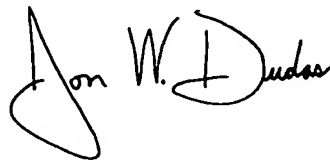
In column 19, line 67, in Claim 4, after "BMC" delete "controller".

In column 20, line 46, in Claim 12, after "Inter-Integrated" insert -- Circuit --.

In column 22, line 3, in Claim 19, after "Inter-Integrated" insert -- Circuit --.

Signed and Sealed this

Fourteenth Day of October, 2008

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looped initial "J" and a distinct "D" at the end.

JON W. DUDAS
Director of the United States Patent and Trademark Office